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OVERLAY REGISTRATION CONTROL SYSTEM AND METHOD EMPLOYING  
MULTIPLE PILOT LOT IN-LINE OVERLAY REGISTRATION MEASUREMENT

BACKGROUND OF THE INVENTION

1. Field of the Invention

5           The present invention relates generally to overlay registration control when fabricating microelectronic products. More particularly, the present invention relates to systems and methods for efficient overlay registration control when fabricating microelectronic products.

10   2. Description of the Related Art

          Microelectronic products are typically fabricated employing multiple overlying levels of interconnected patterned conductor layers which are separated by dielectric layers. When fabricating microelectronic products, it is important that the  
15   multiple overlying interconnected patterned conductor layers are properly registered with respect to each other such as to assure proper functionality of a microelectronic product.

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While overlay registration of patterned conductor layers is thus essential in the microelectronic product fabrication art, overlay registration of patterned conductor layers in nonetheless not entirely without problems in the microelectronic product  
5 fabrication art. In that regard, it is often difficult to provide efficient overlay registration control when fabricating microelectronic products, and in particular when initially fabricating new product order lots within a microelectronic fabrication facility. New product order lots are generally  
10 intended as products lots which might employ a novel technology limitation, such as linewidth or areal density of patterned conductor layers.

It is thus desirable in the microelectronic product fabrication art to provide apparatus, systems and methods for  
15 efficient overlay registration control when fabricating microelectronic products. It is towards the foregoing object that the present invention is directed.

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Various apparatus, systems and methods have been disclosed in the microelectronic product fabrication art for controlling overlay registration when fabricating microelectronic products.

5           Included but not limiting among the apparatus, systems and methods are those disclosed within: (1) Cresswell et al., in U.S. Patent No. 5,923,041 (a self-correcting image sensor method which provides for overlay control when fabricating a microelectronic product); and (2) Toprac et al., in U.S. Patent No.  
10   6,405,096 (an apparatus and method for run-to-run control of overlay registration when fabricating a microelectronic product).

Desirable in the microelectronic product fabrication art are additional apparatus, systems and methods for controlling overlay registration when fabricating microelectronic products.

15           It is towards the foregoing object that the present invention is directed.

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#### SUMMARY OF THE INVENTION

A first object of the invention is to provide a system and a method for controlling overlay registration when fabricating a microelectronic product.

5           A second object of the invention is to provide a system and a method in accord with the first object of the invention, wherein the overlay registration is efficiently controlled.

10           In accord with the objects of the invention, the invention provides a method for controlling overlay registration when fabricating a microelectronic product and a system for controlling overlay registration when fabricating the microelectronic product.

15           In accord with the invention, the method first provides a photolithographic process cell comprising a photolithographic process tool, a photolithographic measurement tool and a computer controller. The method also provides for introducing into the photolithographic process cell a new product order comprising a

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first lot and at least one additional lot, each having an overlay registration offset which is recorded in the computer controller. The method also provides for dividing the first lot into a first pilot lot and a second pilot lot to be sequentially processed in  
5 the photolithographic process tool and measured in the photolithographic measurement tool while being controlled by the computer controller. The method next provides for processing the first pilot lot in-line automatically within photolithographic process tool, measuring the overlay registration thereof within the  
10 photolithographic measurement tool and assuring conformance thereof with the computer controller. The method further provides for processing the second pilot lot in-line automatically within the photolithographic process tool, measuring the overlay registration thereof within the photolithographic measurement tool and assuring  
15 conformance thereof with the computer controller. Finally, the method provides for processing the at least one additional lot in-line automatically within the photolithographic process tool.

The method in accord with the invention contemplates a system in accord with the invention.

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The invention provides a system and a method for controlling overlay registration when fabricating a microelectronic product, wherein the overlay registration is efficiently controlled.

5           The invention realizes the foregoing object within the context of fabricating a new product order of a microelectronic product comprising a first lot and at least one additional lot by dividing the first lot into a first pilot lot and a second pilot lot. Each of the first pilot lot and the second pilot lot is  
10   processed in-line automatically within a photolithographic process tool, has its overlay registration measured within a photolithographic measurement tool and conformance thereof assured with a computer controller, prior to processing the at least one additional lot in-line automatically within the photolithographic  
15   process tool.

# BRIEF DESCRIPTION OF THE DRAWINGS

The objects, features and advantages of the invention are understood within the context of the Description of the Preferred Embodiment, as set forth below. The Description of the Preferred Embodiment is understood within the context of the accompanying drawings, which form a material part of this disclosure, wherein:

Fig. 1 shows a schematic block diagram illustrating a system in accord with the present invention.

Fig. 2 shows a schematic block diagram illustrating in greater detail a system in accord with the present invention.

Fig. 3 shows a schematic process flow diagram of a method in accord with the present invention.

DESCRIPTION OF THE PREFERRED EMBODIMENT

The invention provides a system and a method for controlling overlay registration when fabricating a microelectronic product, wherein the overlay registration is efficiently  
5 controlled.

The invention realizes the foregoing object within the context of fabricating a new product order of a microelectronic product comprising a first lot and at least one additional lot by dividing the first lot into a first pilot lot and a second pilot  
10 lot. Within the invention, each of the first pilot lot and the second pilot lot is processed in-line automatically within a photolithographic process tool, has its overlay registration measured in-line automatically within a photolithographic measurement tool and conformance thereof assured with a computer  
15 controller, prior to processing at least one additional lot of the new product order in-line automatically within the photolithographic process tool.



Fig. 1 shows a schematic block diagram of a system in accord with the invention.

Fig. 1 illustrates a new product order 16 which is desired to be fabricated within a microelectronic fabrication facility. The new product order 16 comprises: (1) a first new product order lot 16a, as well as; (2) at least one additional new product order lot 16b. The system as illustrated in Fig. 1 also comprises a photolithographic process cell 10 which comprises a photolithographic process tool 11, a photolithographic measurement tool 12 and a computer controller 14 which controls in-line and automatically the photolithographic process tool 11 and the photolithographic measurement tool 12.

Within the invention, the new product order 16 which comprises the first new product order lot 16a and the at least one additional new product order lot 16b may comprise a microelectronic product selected from the group including but not limited to semiconductor products and ceramic substrate products. Typically, each of the first new product order lot 16a and the at least one

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additional new product order lot 16b comprises up to about 50 substrates carried in an individual substrate carrier.

Within the invention, the photolithographic process tool 11 is typically selected from the group including but not limited to a photolithographic scanner process tool and a photolithographic stepper process tool. Either of foregoing photolithographic process tools is intended to be employed in photoexposing a blanket photoresist layer formed over each substrate within the new product order 16 such as to form a patterned photoresist layer which is employed as an etch mask layer for forming a patterned layer from a blanket layer formed beneath the patterned photoresist layer.

Within the invention, the photolithographic measurement tool 12 is an overlay registration measurement tool which is intended to measure an overlay registration offset of a patterned photoresist layer with respect to an underlying patterned layer or overlay registration fiducial within a substrate within the first new product order lot 16a or the series of additional new product order lots 16b.

Within the invention, the computer controller 14 is otherwise generally conventional in the microelectronic product fabrication art and may comprise a personal computer or a network server which is intended to control each of the photolithographic process tool 11 and the photolithographic measurement tool 12. The computer controller 14 may also control the flow of the first new product order lot 16a and the at least one additional new product order lot 16b through the photolithographic process tool 11 and the photolithographic measurement tool 12. The computer controller 14 is otherwise specifically programmed to effect the objects of the invention. In that regard, the invention is intended to provide for an efficient introduction of the new product order 16 into a microelectronic fabrication facility having contained therein the photolithographic process cell 10.

Fig. 2 shows a schematic block diagram illustrating in greater detail a system in accord with the present invention.

Fig. 2 illustrates the photolithographic process tool 11 and the photolithographic measurement tool 12 and the new product

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order lots 16 as illustrated in Fig. 1. Fig. 2 also illustrates a photolithographic process tool in-line interface 11' associated with the photolithographic process tool 11 and a photolithographic measurement tool in-line interface 12' associated with the photolithographic measurement tool. Fig. 2 also illustrates the computer controller 14 in greater detail, such as to includes a photolithographic cell server 14a having associated therewith a photolithographic process database 14b. The photolithographic cell server 14a and the photolithographic process database 14b also have associated therewith a user interface 14c, such as a graphical user interface and a keyboard user interface, as well as a security interface 14d and a software interface 14e which provide for complete operation of the computer controller 14. Finally, the system as illustrated in Fig. 2 also comprises a work in process database 18a having associated therewith a software interface 18b for purposes of controlling the series of new product order lots 16 as they travel through the photolithographic process tool in-line interface 11' and the photolithographic measurement tool in-line interface 12' and are photolithographically processed and measured to become finished new product order lots 16'.

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Fig. 3 shows a schematic process flow diagram illustrating a series of process steps in accord with a method of the present invention.

Within Fig. 3, both a first new product order lot 30a and  
5 a series of additional new product order lots 30b are introduced into a photolithographic process cell in accord with the systems as illustrated in Fig. 1 and Fig. 2.

In accord with the block which corresponds with reference numeral 32, the first new product order lot is split into a first  
10 pilot lot (i.e., a parent lot) and a second pilot lot (i.e., a child lot).

In accord with the block which corresponds with reference numeral 34, an in-line automatic photolithographic computer controller for the photolithographic process cell is programmed to  
15 define the first pilot lot, the second pilot lot, photolithographic process control parameters (including overlay registration offset) and an expiration date beyond which recalibration and

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requalification is needed for a photolithographic process tool and a photolithographic measurement tool within the photolithographic process cell. The in-line automatic photolithographic computer controller is also programmed with the additional new product order  
5 lots 30b information.

In accord with the block which corresponds with reference numeral 36, the first pilot lot is processed in-line automatically within a photolithographic process tool, such as but not limited to a scanner or a stepper. In addition, and in accord with the block  
10 which corresponds with reference numeral 38, an overlay registration is measured in-line automatically for the first pilot lot.

If the overlay registration measurement is not in accord with specification expectations, an overlay registration offset in  
15 accord with the block which corresponds with reference numeral 34 is revised. Such revision is typically effected incident to manual control. If the overlay registration measurement is in accord with specification expectations, and in accord with the block which

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corresponds with reference numeral 40, only then is the second pilot lot is processed in-line automatically within the photolithographic process tool.

5 In accord with the block which corresponds with reference numeral 42 an overlay registration offset of the second pilot lot is measured in-line automatically within the photolithographic measurement tool.

10 If the overlay registration measurement is not in accord with specification expectations, an overlay registration offset is again revised (typically manually) within the in-line automatic computer controller for photoexposing a blanket photoresist layer formed upon a substrate within the second pilot lot. If the overlay registration is in accord with specification expectations, and in accord with the blocks which corresponds with reference  
15 numerals 44 and 46, the first pilot lot and the second pilot lot qualification are completed and the additional new product order lots 30b are then (and only then) allowed to process in-line automatically within the photolithographic processing tool to

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provide a series of finished new product order lots 30'. The series of additional new product order lots 30b need not necessarily be processed in-line automatically within the photolithographic measurement tool, presuming that an expiration  
5 date has not been exceeded beyond which recalibration and requalification of the photolithographic process tool and the photolithographic measurement tool is required. Such recalibration and requalification will typically reset the in-line automatic computer controller to a pilot mode, and the process steps as  
10 illustrated within the schematic block diagram of Fig. 3 will be repeated, although not necessarily within the context of a new product order.

The present invention thus provides for an in-line and automatic qualification of a first new product order lot (which is  
15 divided into a first pilot lot and a second pilot lot) with respect to a photolithographic process tool and a photolithographic measurement tool, prior to allowing for additional in-line and automatic processing of additional new product order lots within a microelectronic fabrication facility. The invention provides value



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insofar as the invention avoids the need for a separate and independent pilot lot procedure and associated separate and independent data and information management resources directed towards an independent qualification of a new product order within  
5 a photolithographic process tool and a photolithographic measurement tool.

The preferred embodiment of the invention is illustrative of the invention rather than limiting of the invention. Revisions and modification may be made to apparatus and procedures in accord  
10 with the preferred embodiment of the invention while still providing a system in accord with the invention and a method in accord with the invention, further in accord with the accompanying claims.